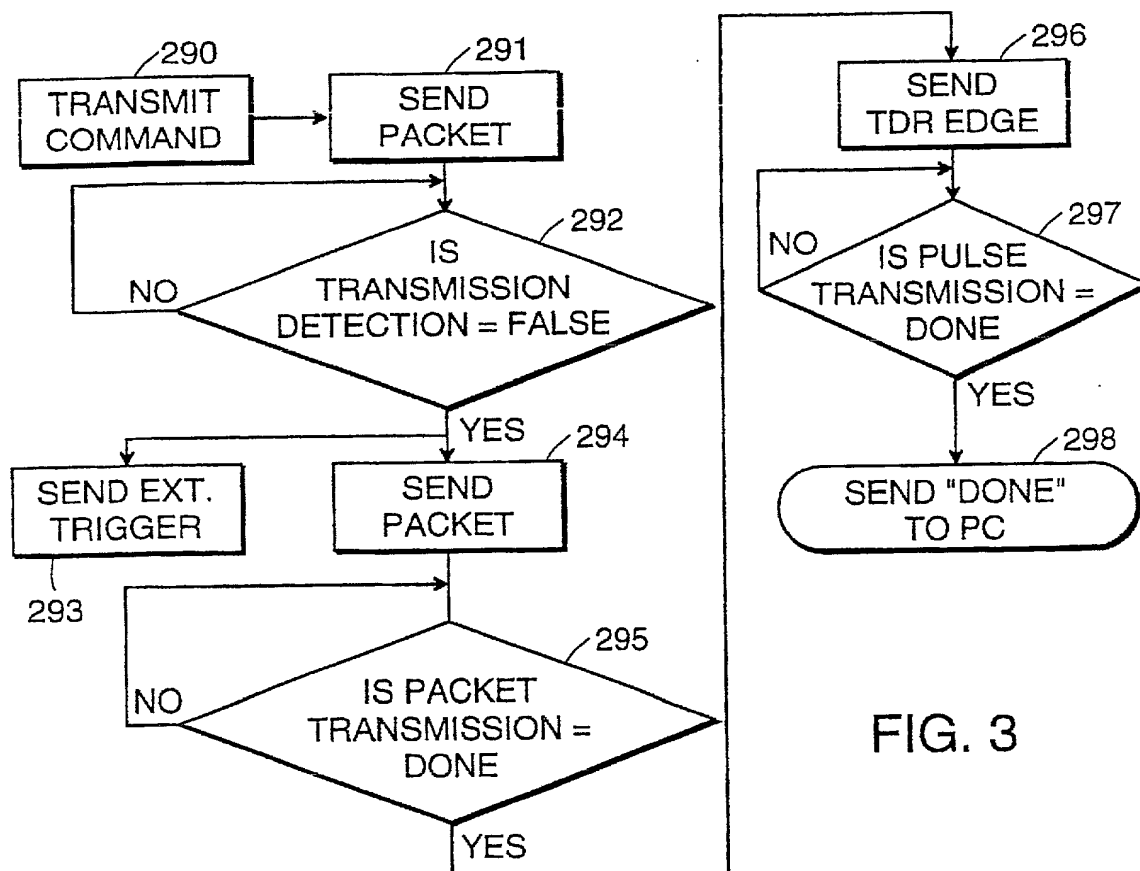
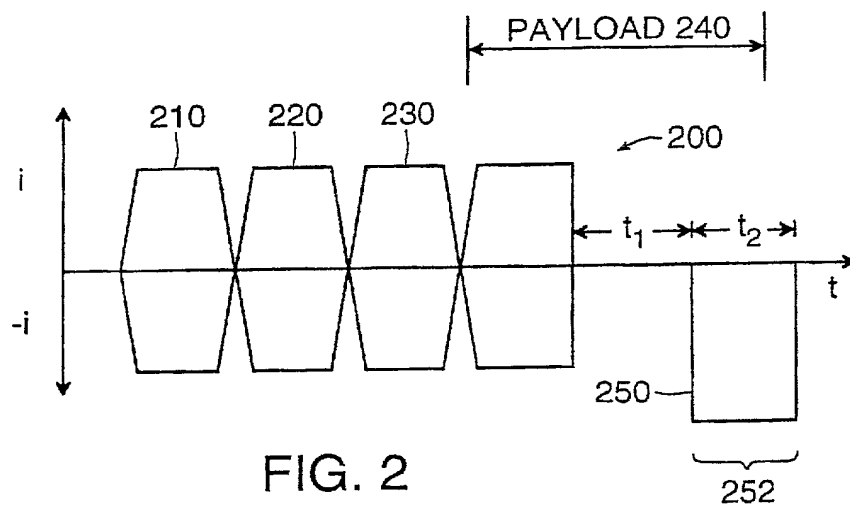


FIG. 1B



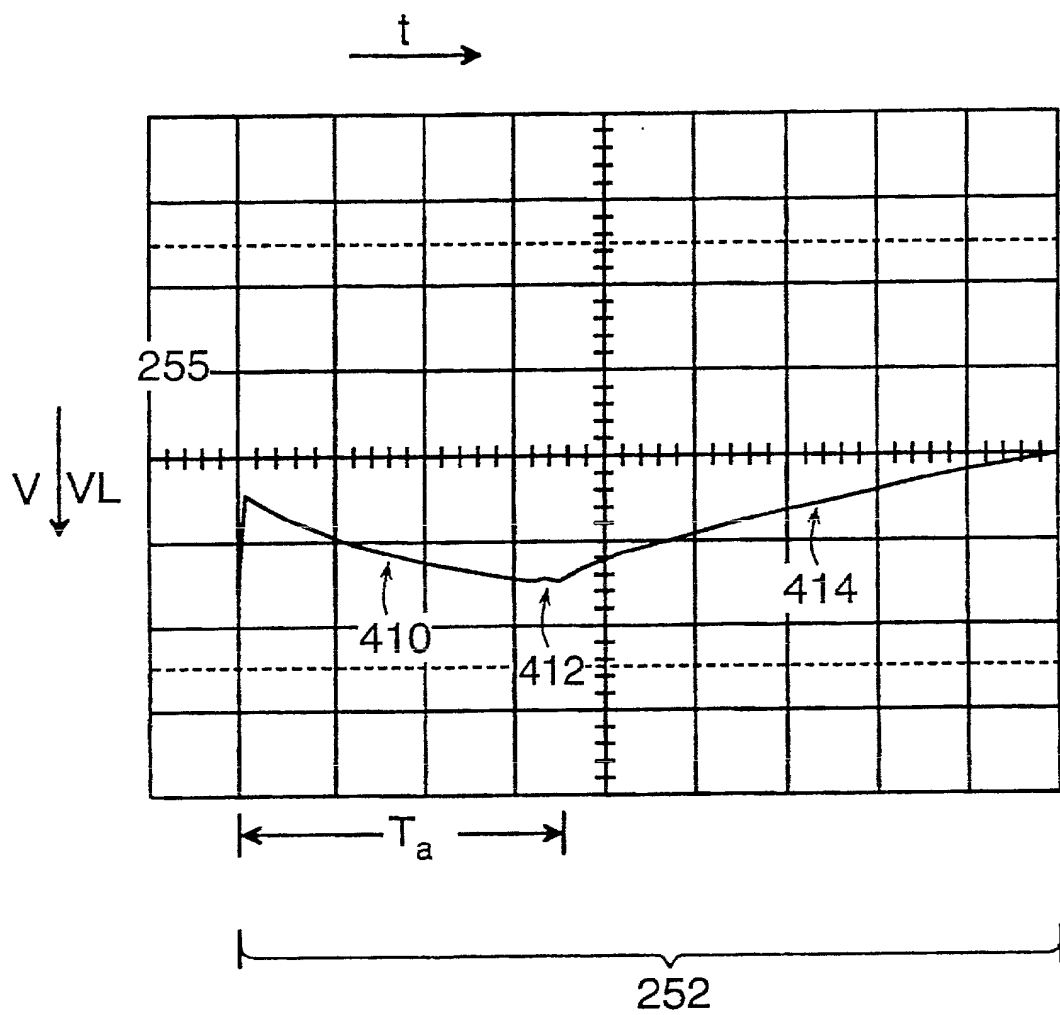


FIG. 4

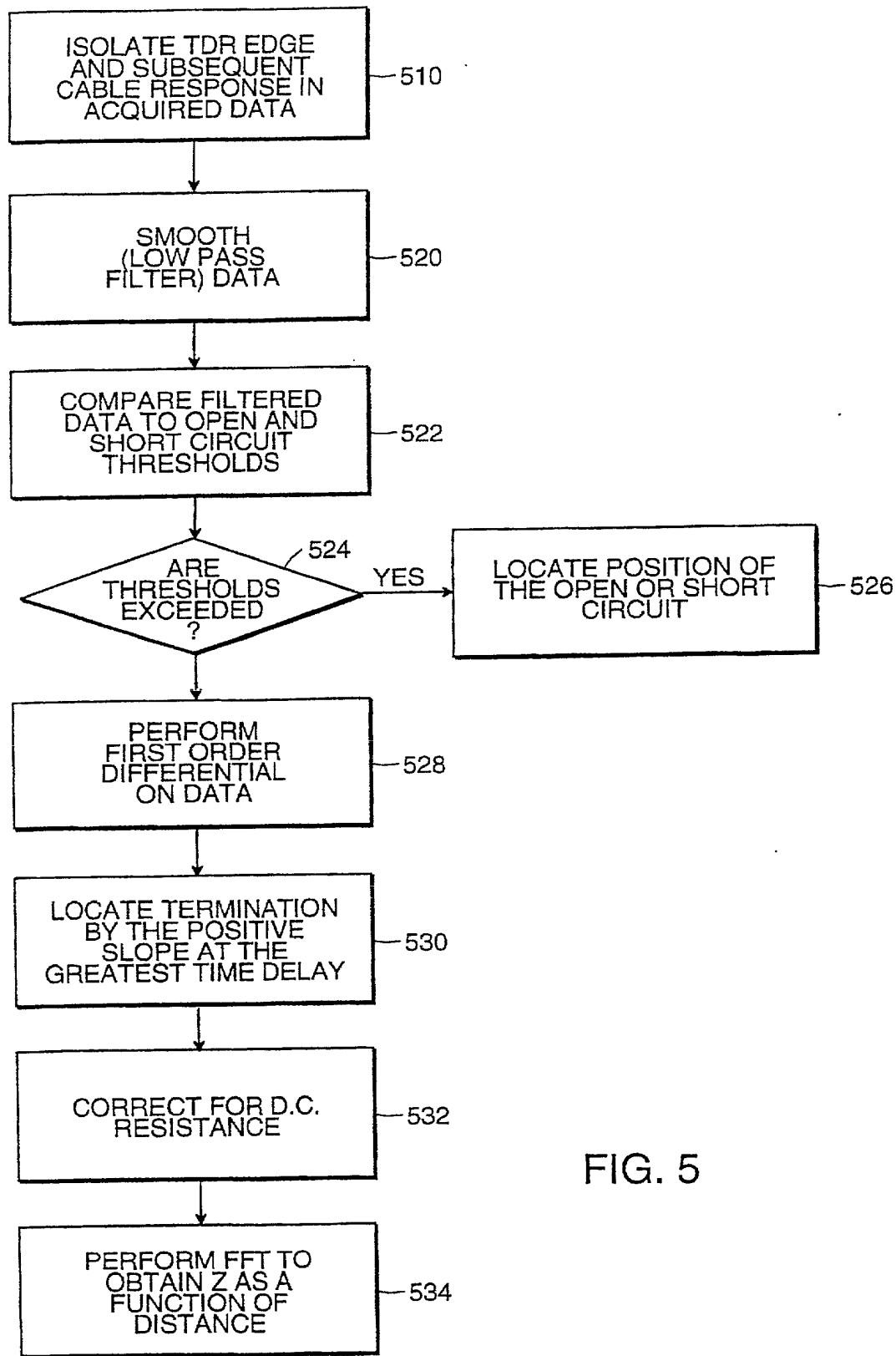


FIG. 5

0981051.050T2860

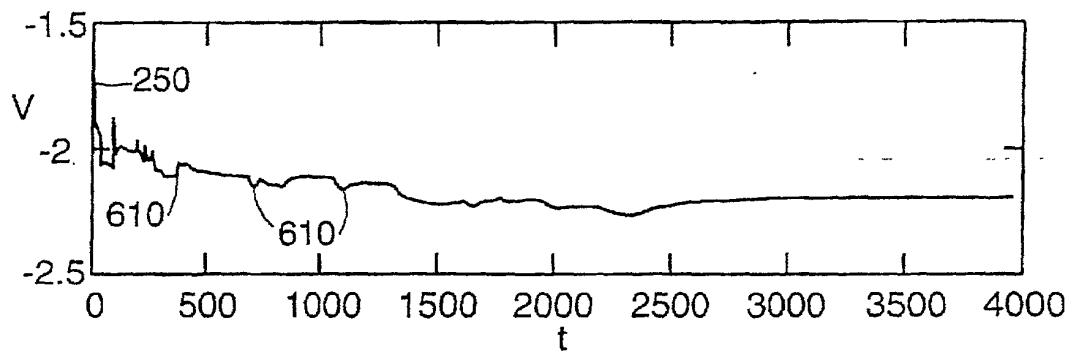


FIG. 6

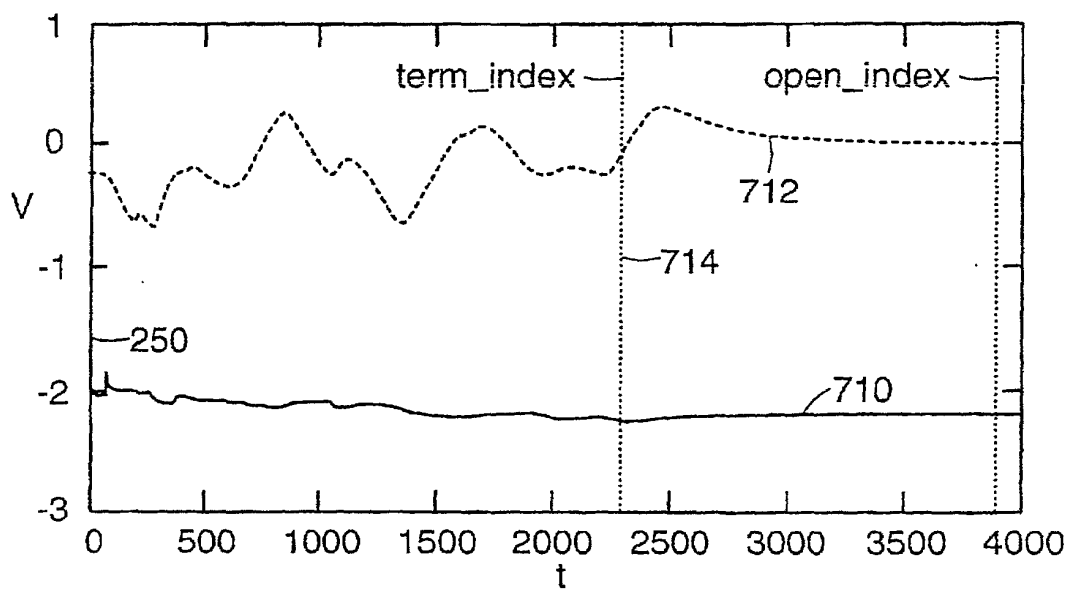


FIG. 7

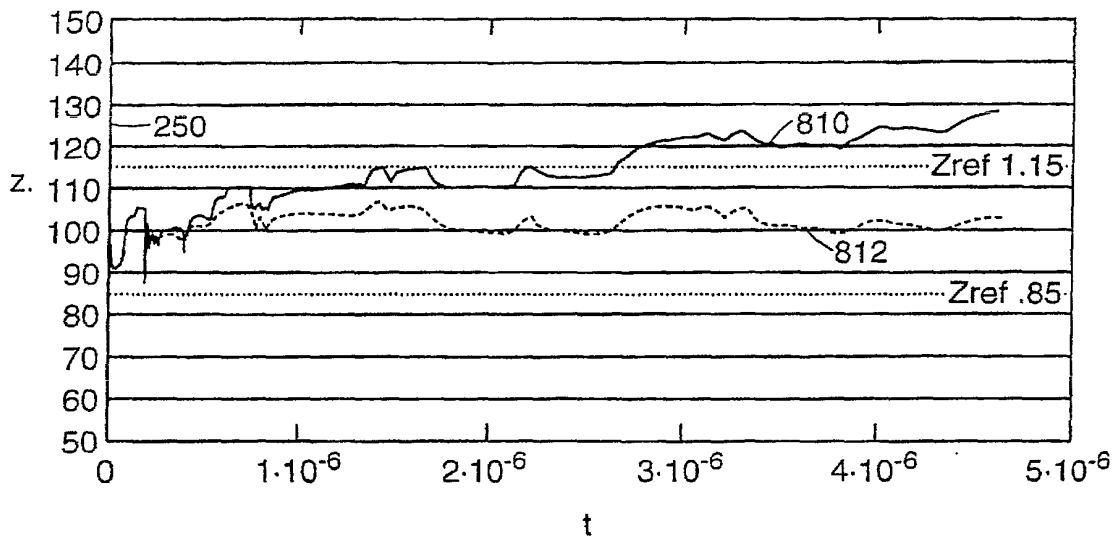


FIG. 8

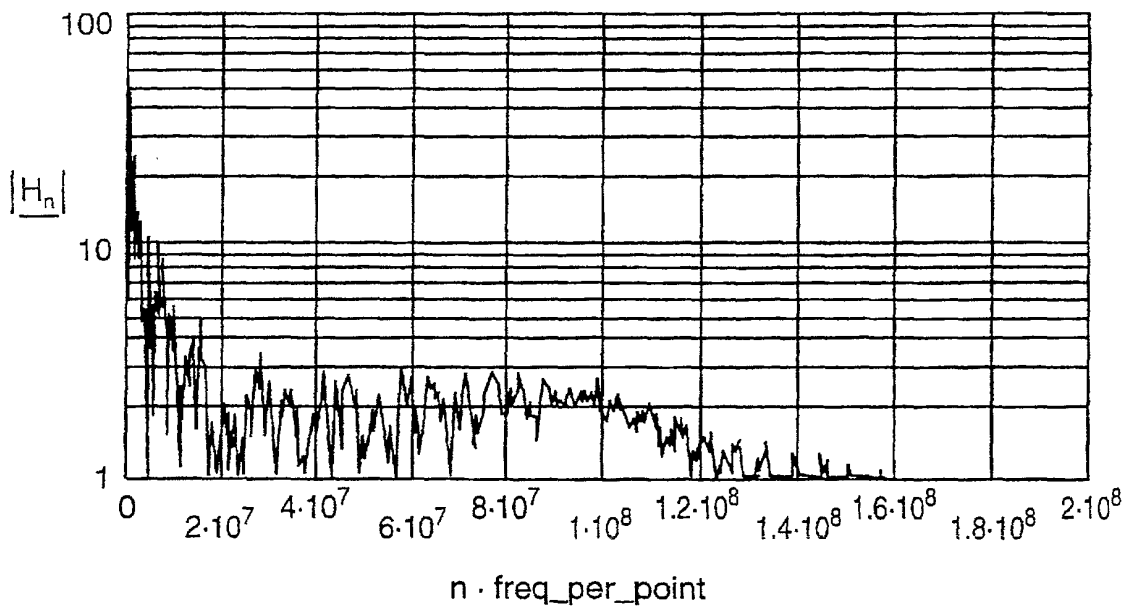


FIG. 9

TEST 20 ESOT 2860

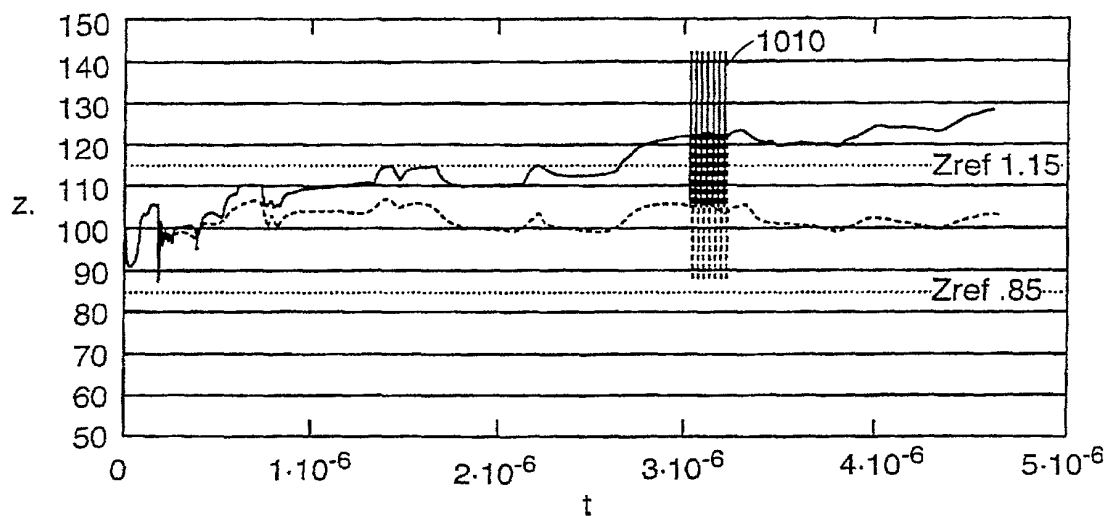


FIG. 10

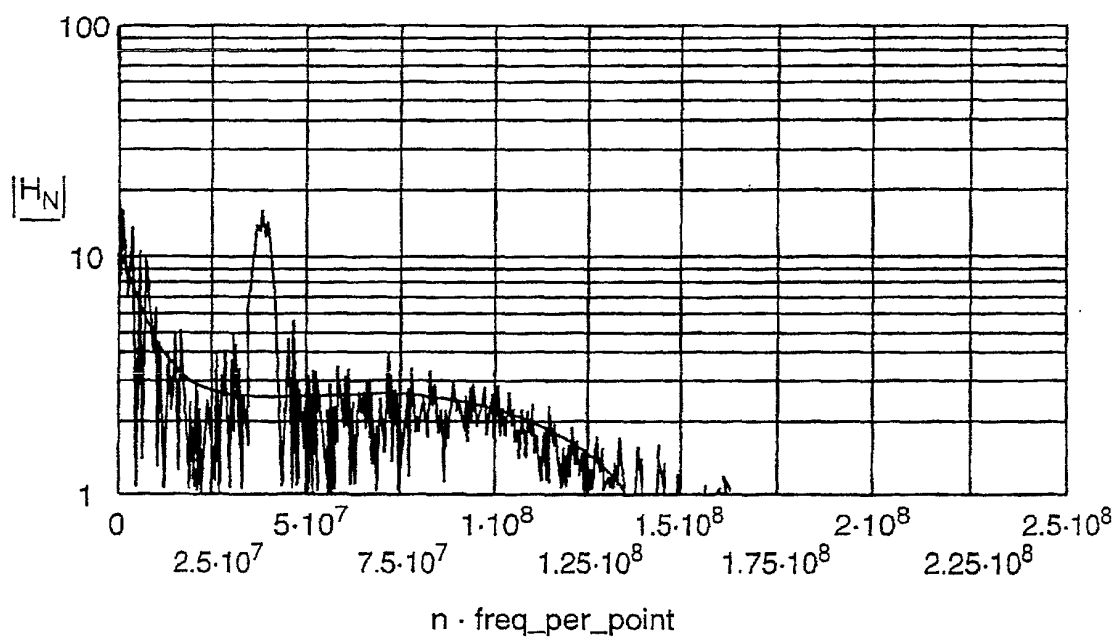


FIG. 11





```

graph TD
    1310[OPEN OR CLOSE  
CIRCUIT NODE-  
SIDE OF LINK] --> 1320[INJECT TDR SIGNAL]
    1320 --> 1330[DETECT HUB-SIDE  
RESPONSE]
    1330 --> 1340[STORE HUB-SIDE  
RESPONSE TO ISOLATE  
NODE-SIDE RESPONSE  
LATER]

```

FIG. 13

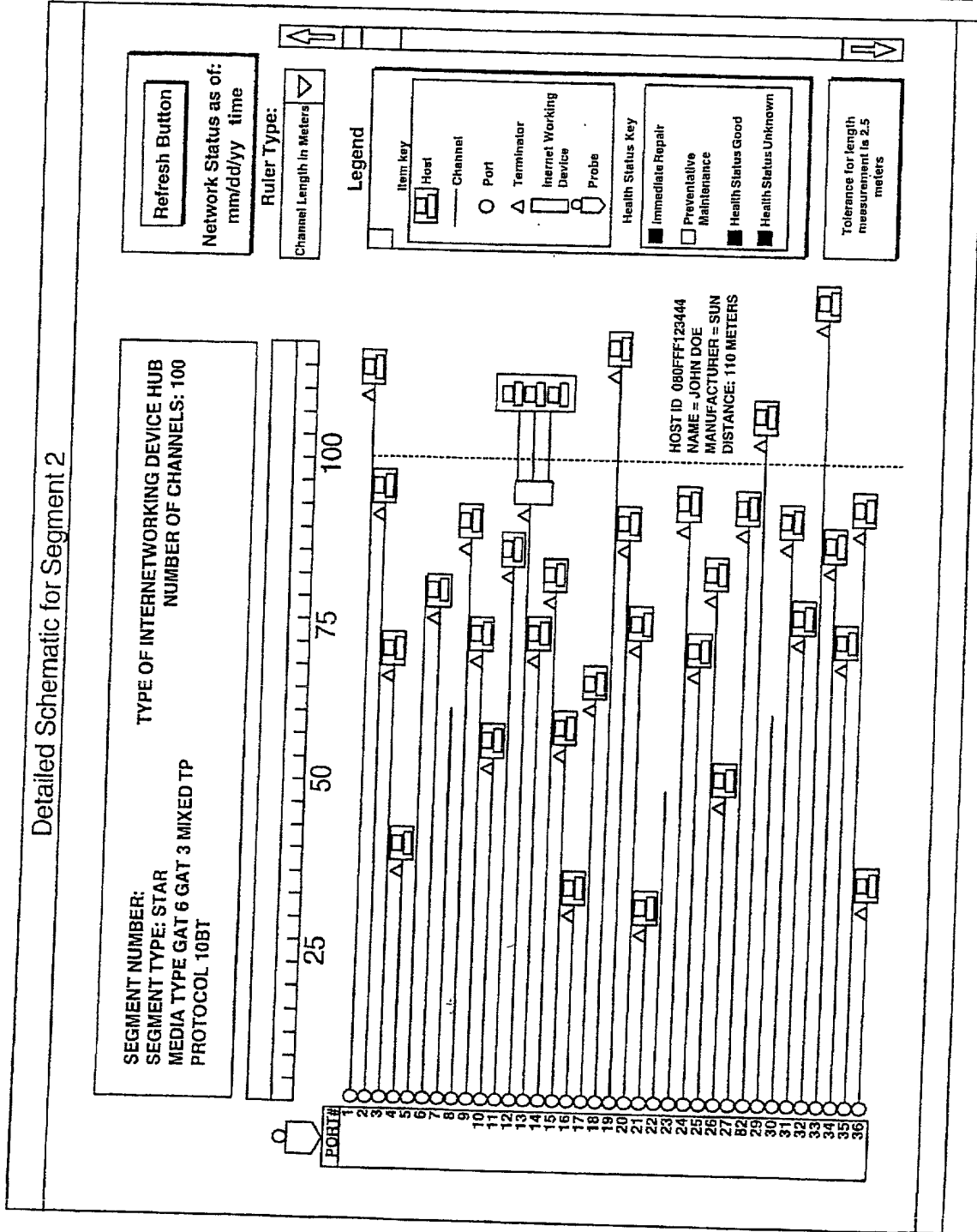


FIG. 14